

METHOD OF MAKING AN INTEGRATED CIRCUIT

Abstract of the Disclosure

5 An integrated circuit is made by utilizing a delay and/or capacitance
model and/or setup/hold time model which provides for the ability to isolate
issues of transistor performance, metallization capacitance, metallization
resistance, power supply voltage, and temperature for the individual design
blocks that make up the integrated circuit. This is achieved by utilizing an
10 equation representative of these performance characteristics as certain variables
in the equation. The equation also has constants which are determined by first
running the design blocks through a standard circuit simulator. The result is a
different set of these constants for each design block. Various signal paths are
made up of various design blocks so that each path can be analyzed by
15 analyzing the performance of the individual blocks that make up the path.
Thus, areas of improvement for the design blocks are more easily identified
prior to actually making the integrated circuit.